

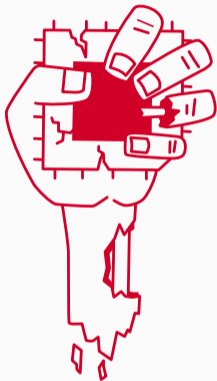
ZombieLoad

Cross-Privilege-Boundary Data Sampling

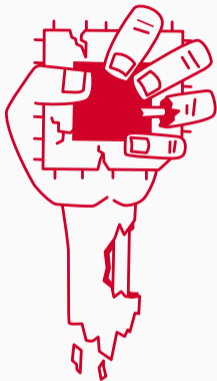
**Michael Schwarz¹, Moritz Lipp¹, Daniel Moghimi², Jo Van Bulck³, Julian Stecklina⁴,
Thomas Prescher⁴, Daniel Gruss¹**

November 11, 2019

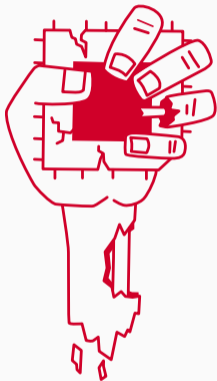
¹ Graz University of Technology, ² Worcester Polytechnic Institute, ³ imec-DistriNet, KU Leuven, ⁴ Cyberus Technology



- A new Meltdown-type **transient-execution attack**



- A new Meltdown-type **transient-execution attack**
- Leaks data on Intel CPUs



- A new Meltdown-type **transient-execution attack**
- Leaks data on Intel CPUs
- Really new? Published in May 2019...

BBC

Intel ZombieLoad bug fix to slow data centre computers

THE VERGE

ZombieLoad attack lets hackers steal data from Intel chips

FORTUNE

'ZombieLoad' Flaw Lets Hackers Crack Almost Every Intel Chip Back to 2011. Why's It Being Downplayed?

How-To Geek

Only New CPUs Can Truly Fix ZombieLoad and Spectre



ZombieLoad

🛡️ CVE-2018-12130

🛡️ CVE-2019-11091



ZombieLoad

- 🚫 CVE-2018-12130
- 🚫 CVE-2019-11091



RIDL

- 🚫 CVE-2018-12127
- 🚫 CVE-2018-12130
- 🚫 CVE-2019-11091



ZombieLoad

- 🔒 CVE-2018-12130
- 🔒 CVE-2019-11091



RIDL

- 🔒 CVE-2018-12127
- 🔒 CVE-2018-12130
- 🔒 CVE-2019-11091



Fallout

- 🔒 CVE-2018-12126



ZombieLoad

- 🔒 CVE-2018-12130
- 🔒 CVE-2019-11091
- 🔒 **CVE-2019-11135**



RIDL

- 🔒 CVE-2018-12127
- 🔒 CVE-2018-12130
- 🔒 CVE-2019-11091



Fallout

- 🔒 CVE-2018-12126



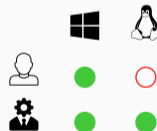
Variant 1

Kernel Mapping



Variant 3

Microcode-Assisted Page-Table Walk





Variant 1

Kernel Mapping



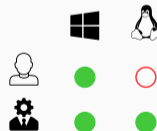
Variant 2

Transactional
Asynchronous Abort



Variant 3

Microcode-Assisted
Page-Table Walk





- Variant 2 embargoed until November 12, 2019

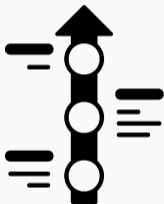


- Variant 2 embargoed until November 12, 2019
- Only variant without hardware mitigations



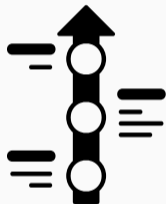
- Variant 2 embargoed until November 12, 2019
 - Only variant without hardware mitigations
- Works on MDS-resistant Cascade Lake CPUs

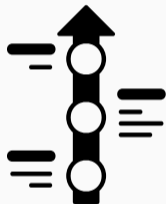
March 28, 2018 We report Meltdown on fill buffer (CVE-2019-11091)



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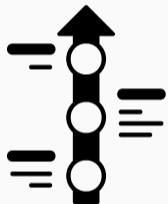




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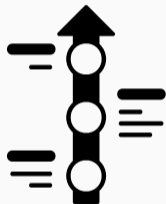
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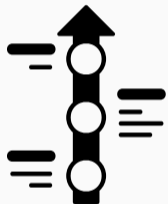
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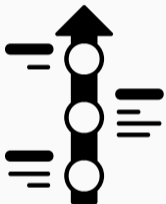
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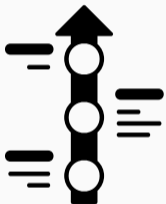
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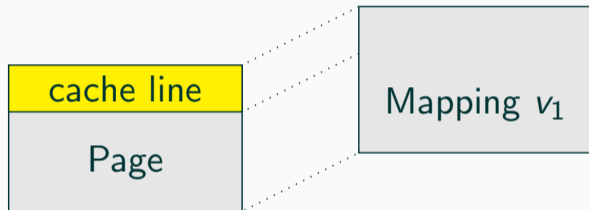
→ Additional embargo until November 12, 2019 (TAA)

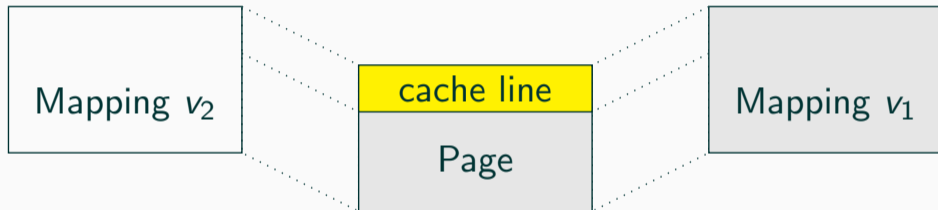


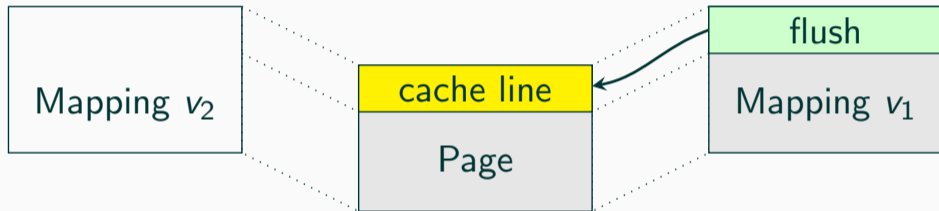
	Fill Buffer, Load Ports, ?	Fill Buffer, Load Ports	Store Buffer
	All loads & stores	Uncached loads & stores	Stores
	✓	✗	✗
	✓	✗ (before Cascade Lake)	✗ (before Cascade Lake)

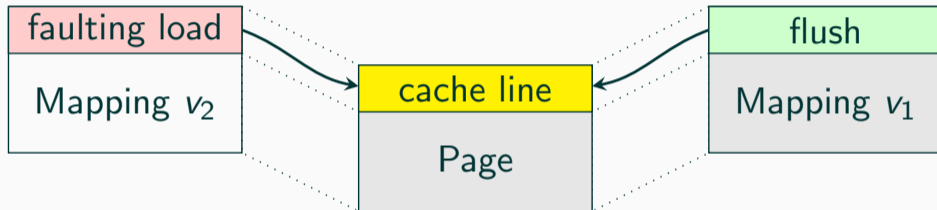


ZombieLoad works **despite** software **mitigations** and even on **MDS-resistant** CPUs (e.g., Cascade Lake)





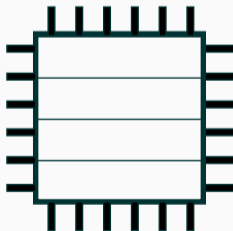




User Memory

	A	B
C	D	E
F	G	H
I	J	K
L	M	N
O	P	Q
R	S	T
U	V	W
X	Y	Z

```
char value = faulting[0]
```



User Memory

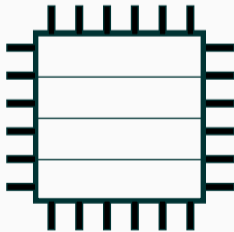
	A	B
C	D	E
F	G	H
I	J	K
L	M	N
O	P	Q
R	S	T
U	V	W
X	Y	Z

```
char value = faulting[0]
```

↓
mem[value]

Out of order

K



User Memory

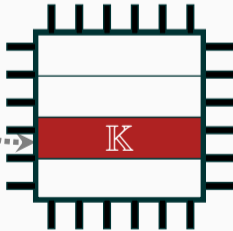
	A	B
C	D	E
F	G	H
I	J	K
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O	P	Q
R	S	T
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```
char value = faulting[0]
```

```
mem[value]
```

Out of order

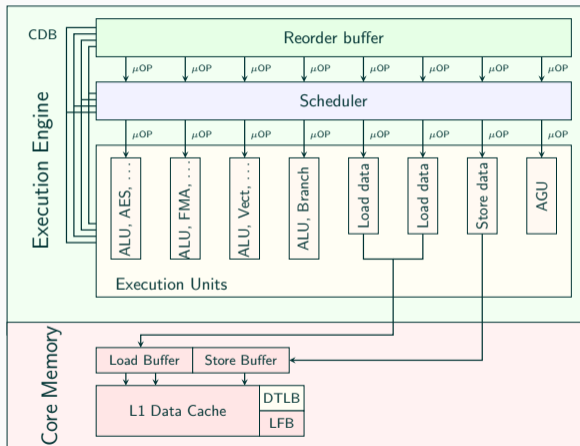
K

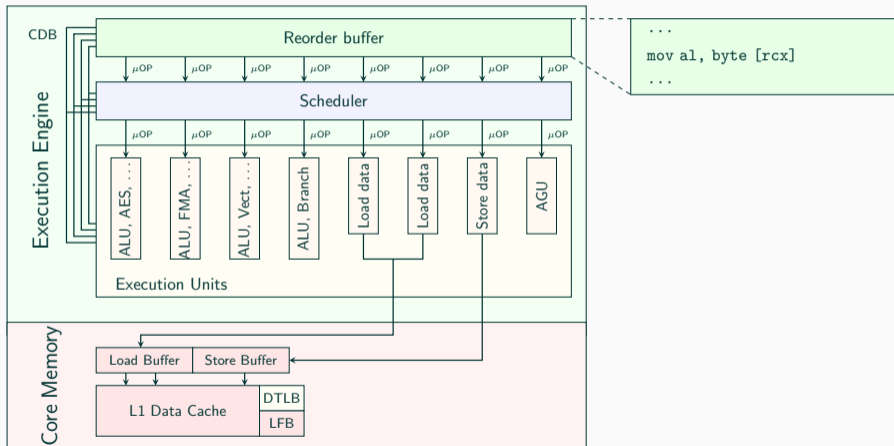


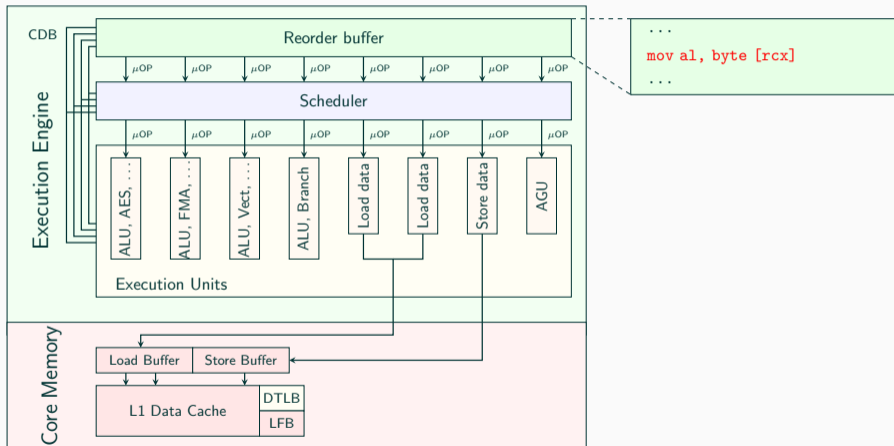
NOISE

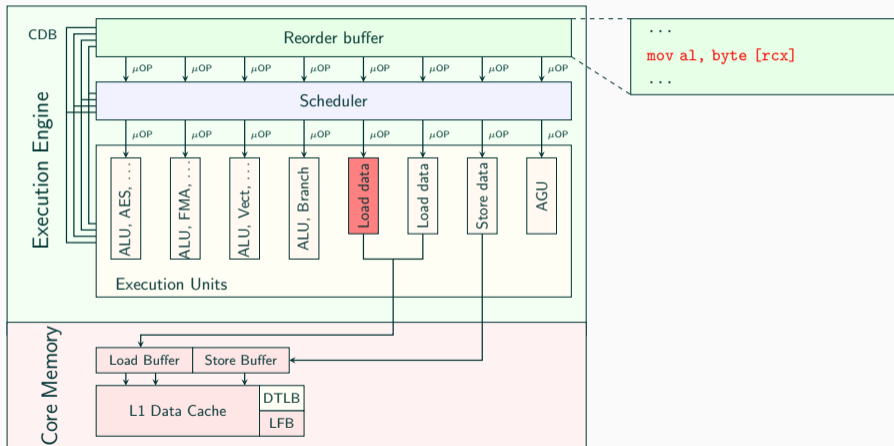
There is no noise.

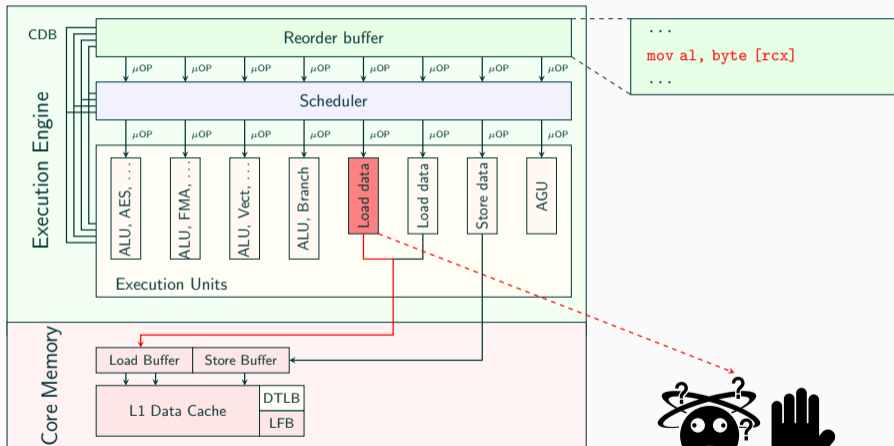
Noise is just
someone else's data

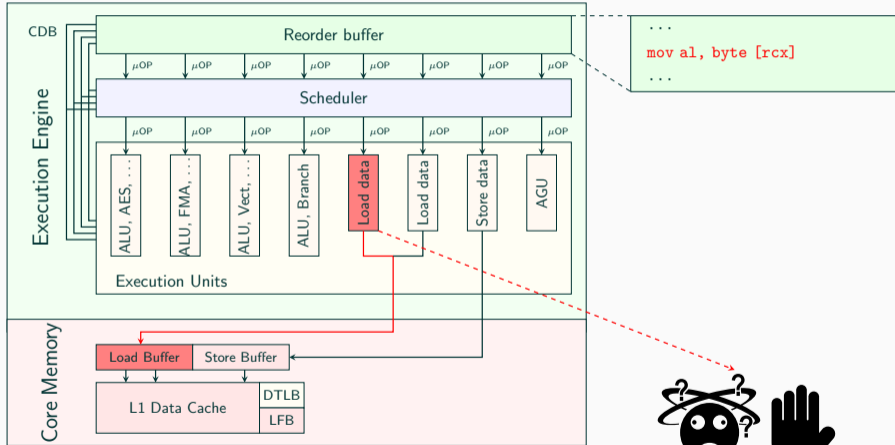


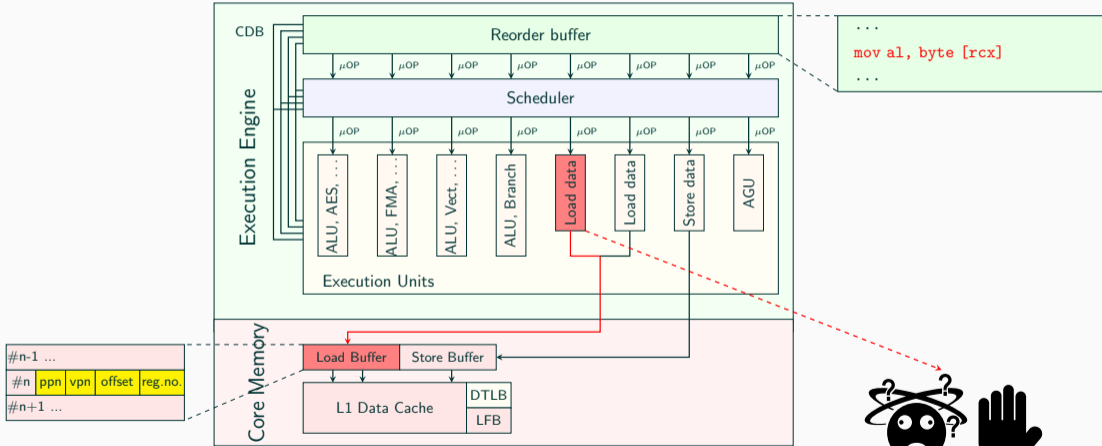


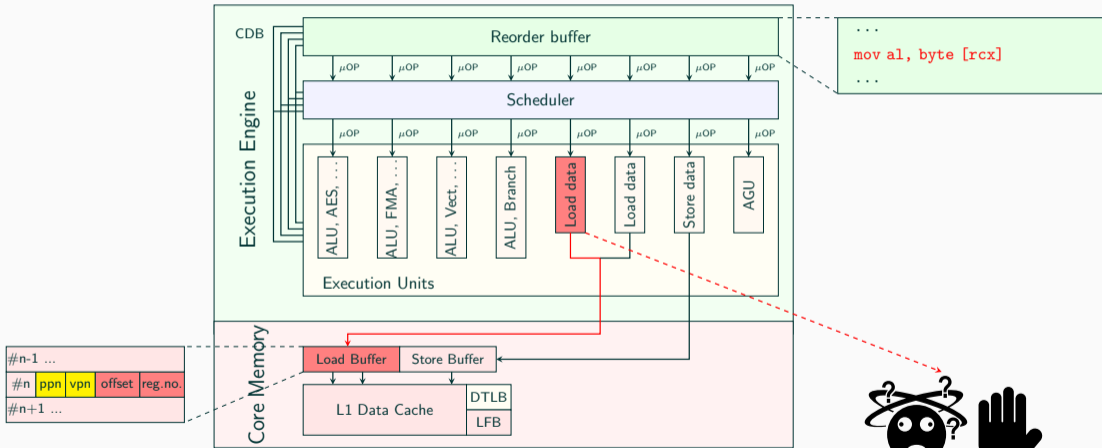


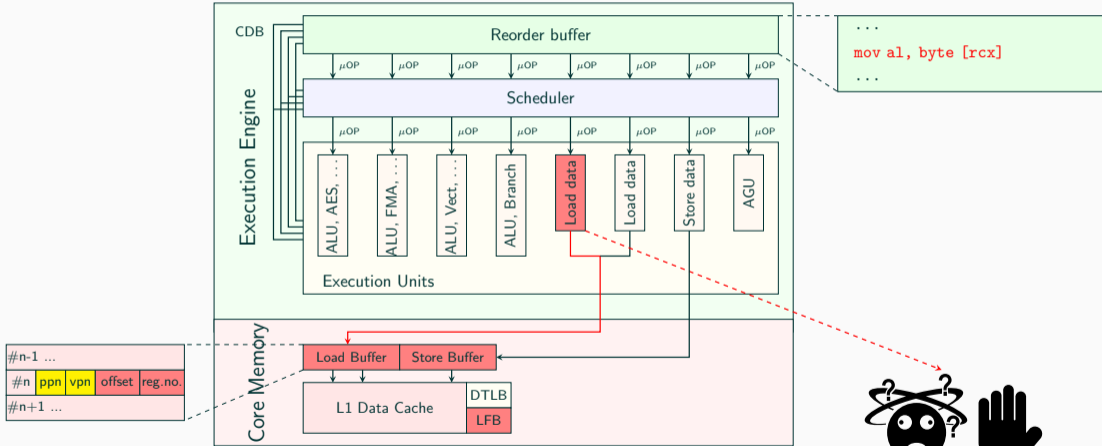




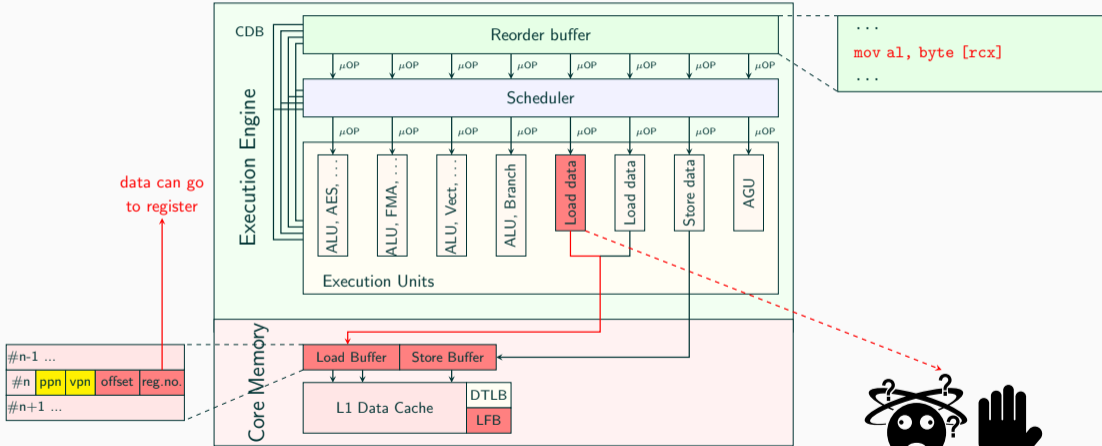








#n-1 ...				
#n	ppn	vpn	offset	reg.no.
#n+1 ...				





- Complex situations handled in microcode



- **Complex** situations handled in **microcode**
 - Setting accessed/dirty bit



- **Complex** situations handled in **microcode**
 - Setting accessed/dirty bit
 - TSX abort + rollback



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- **Complex** situations handled in **microcode**
 - Setting accessed/dirty bit
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 - ...
- Load needs to be **re-issued**



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 - Setting accessed/dirty bit
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 - ...
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- **Meltdown** effects due to “microarchitectural fault”



- **Complex** situations handled in **microcode**
 - Setting accessed/dirty bit
 - TSX abort + rollback
 - ...
- Load needs to be **re-issued**
- **Meltdown** effects due to “microarchitectural fault”
- **No** architectural **fault handling** required



- Leak data on **same** and **sibling** hyperthread



- Leak data on **same** and **sibling** hyperthread



Applications



- Leak data on **same** and **sibling** hyperthread



Applications



Operating System



- Leak data on **same** and **sibling** hyperthread



Applications



Operating System



SGX Enclave



- Leak data on **same** and **sibling** hyperthread



Applications



Operating System



SGX Enclave



Virtual Machine



- Leak data on **same** and **sibling** hyperthread



Applications



Operating System



SGX Enclave



Virtual Machine

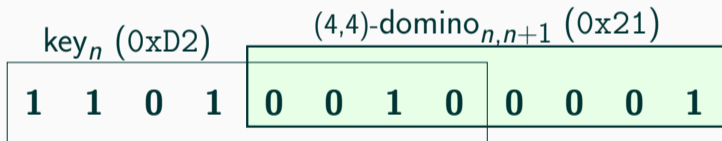


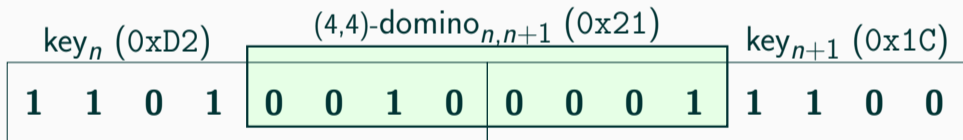
Hypervisor

		Page Number		Page Offset			
Meltdown	51	Physical	12	11 0			
	47	Virtual	12				
Foreshadow	51	Physical	12	11 0			
	47	Virtual	12				
Fallout	51	Physical	12	11 0			
	47	Virtual	12				
ZombieLoad/ RIDL	51	Physical	12	11	6	5 0	
	47	Virtual	12				

key_n (0xD2)

1	1	0	1	0	0	1	0
---	---	---	---	---	---	---	---







AES-NI key



AES-NI key



SGX sealing key



AES-NI key



SGX sealing key



Cross-VM covert
channel



AES-NI key



SGX sealing key



Cross-VM covert
channel



Keyword matching



AES-NI key



SGX sealing key



Cross-VM covert
channel



Keyword matching



URL recovery



AES-NI key



SGX sealing key



Cross-VM covert
channel



Keyword matching



URL recovery



Targeted leakage



Variant 1

Kernel Mapping

5.30 kB/s



Variant 1

Kernel Mapping

5.30 kB/s



Variant 2

Transactional
Asynchronous Abort

39.66 kB/s



Variant 1

Kernel Mapping

5.30 kB/s



Variant 2

Transactional
Asynchronous Abort

39.66 kB/s



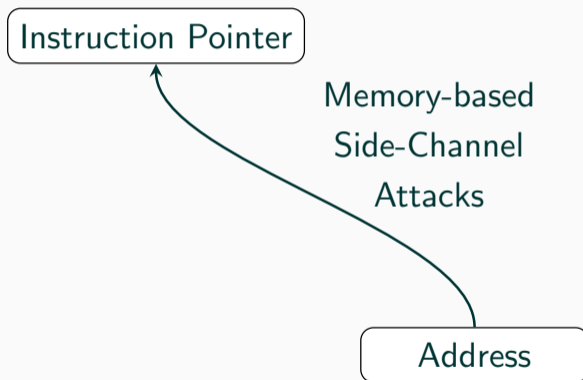
Variant 3

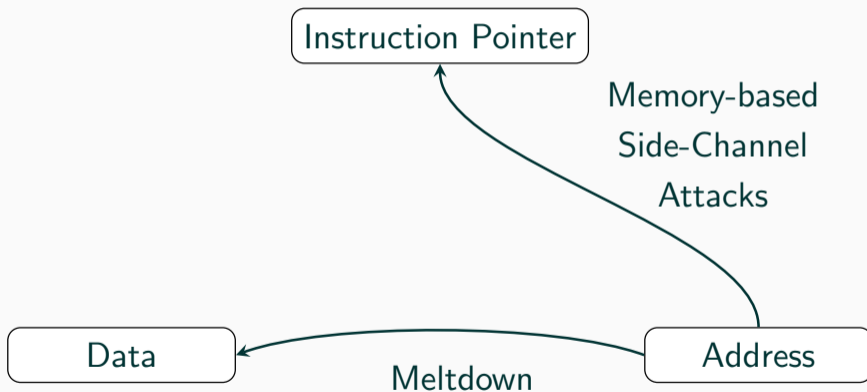
Microcode-Assisted
Page-Table Walk

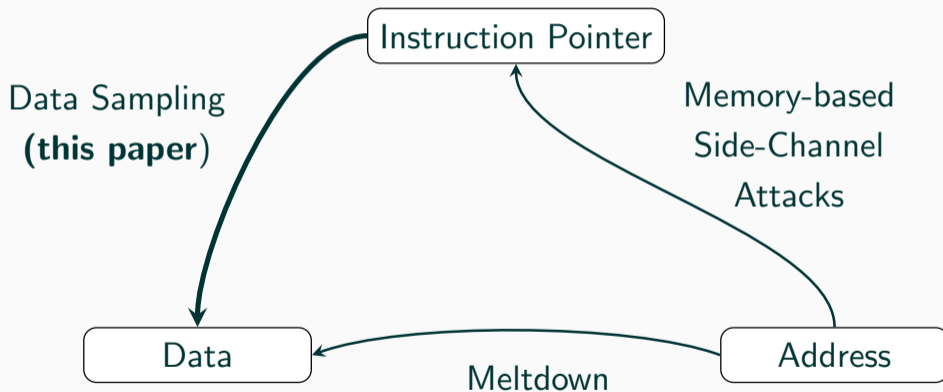
7.73 kB/s

```
michael@hp /tmp/zombieload %
```

Address









- Disable **hyperthreading** or group scheduling



- **Disable hyperthreading** or group scheduling
- **Overwrite** microarchitectural buffers



- **Disable hyperthreading** or group scheduling
- **Overwrite** microarchitectural buffers
 - VERW instruction (microcode update)



- **Disable hyperthreading** or group scheduling
- **Overwrite** microarchitectural buffers
 - VERW instruction (microcode update)
 - Software sequences



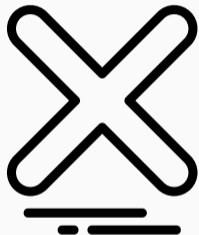
- **Disable hyperthreading** or group scheduling
- **Overwrite** microarchitectural buffers
 - VERW instruction (microcode update)
 - Software sequences
- New CPUs which are not affected

CPU	Meltdown	Foreshadow	RIDL	Fallout	MLPDS	MDSUM
8th/9th gen. Intel Core Coffee Lake	✘	✘	✘	✘	✘	✘
Intel Xeon Cascade Lake	✘	✘	✘	✘	✘	✘



- **Disable hyperthreading** or group scheduling
- **Overwrite** microarchitectural buffers
 - VERW instruction (microcode update)
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CPU	Meltdown	Foreshadow	RIDL	Fallout	MLPDS	MDSUM	ZombieLoad
8th/9th gen. Intel Core Coffee Lake	✘	✘	✘	✘	✘	✘	???
Intel Xeon Cascade Lake	✘	✘	✘	✘	✘	✘	???



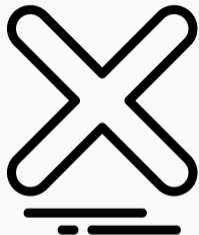
- Variant 2 works on **all CPUs**



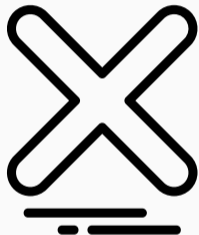
- Variant 2 works on **all CPUs**
- Embargoed until November 12, 2019



- Variant 2 works on **all CPUs**
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- Microcode and software sequences do **not prevent** ZombieLoad



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- Reported on May 16, 2019



- Variant 2 works on **all CPUs**
- Embargoed until November 12, 2019
- Microcode and software sequences do **not prevent** ZombieLoad
- Reported on May 16, 2019
- ZombieLoad might not only leak from fill buffer



- **Disable** hyperthreading



- **Disable** hyperthreading
- Flush **all** buffers on privilege-level change



- **Disable** hyperthreading
- Flush **all** buffers on privilege-level change
 - Fill buffer, store buffer, load ports → VERW



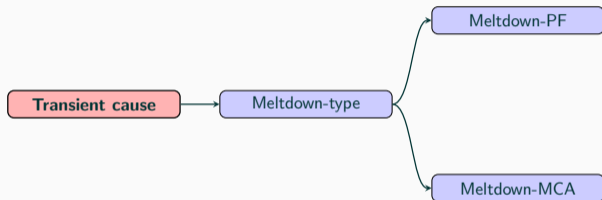
- **Disable** hyperthreading
- Flush **all** buffers on privilege-level change
 - Fill buffer, store buffer, load ports → VERW
 - Flush L1 cache → MSR_IA32_FLUSH_CMD

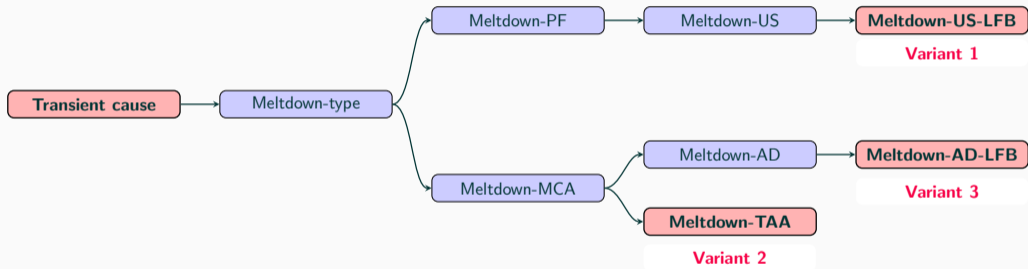


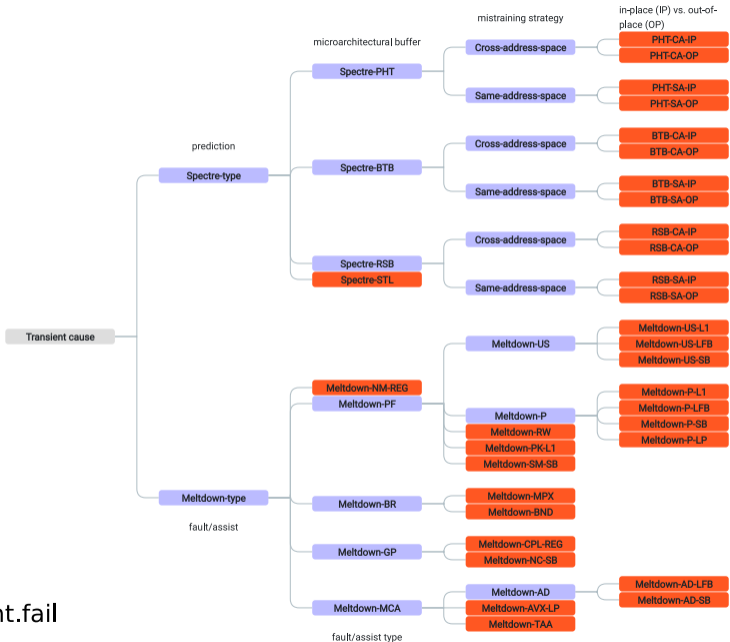
- **Disable** hyperthreading
- Flush **all** buffers on privilege-level change
 - Fill buffer, store buffer, load ports → VERW
 - Flush L1 cache → MSR_IA32_FLUSH_CMD
- **Disable** Intel TSX (MSR_TSX_FORCE_ABORT)

Transient cause







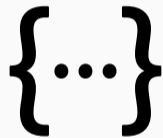


<https://transient.fail>

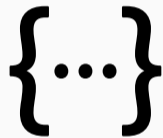


You can find our **proof-of-concept** implementation on:

- <https://github.com/IAIK/ZombieLoad>



- **Transient-execution attacks**: the gift that keeps on giving



- **Transient-execution attacks**: the gift that keeps on giving
- **Class of Meltdown attacks** is larger than expected



- **Transient-execution attacks**: the gift that keeps on giving
- **Class of Meltdown attacks** is larger than expected
- CPUs are deterministic - there is **no noise**

MORITZ LIPP MICHAEL SCHWARZ DANIEL MOGHIMI JO VAN BULCK

ZOMBIELOAD



GRAZ UNIVERSITY OF TECHNOLOGY PRESENTS IN COLLABORATION WITH
WORCESTER POLYTECHNIC INSTITUTE, KU LEUVEN, AND CYBERUS TECHNOLOGY
AN ACM CCS 2019 PAPER "ZOMBIELOAD: CROSS-PRIVILEGE-BOUNDARY DATA SAMPLING"
WRITTEN BY MICHAEL SCHWARZ, MORITZ LIPP, DANIEL MOGHIMI, JO VAN BULCK, JULIAN STECKLINA, THOMAS PRESCHER, DANIEL GRUSS

ZombieLoad

Cross-Privilege-Boundary Data Sampling

Michael Schwarz, Moritz Lipp, Daniel Moghimi, Jo Van Bulck, Julian Stecklina, Thomas Prescher, Daniel Gruss

November 11, 2019

Graz University of Technology